

# PATENT ABSTRACTS OF JAPAN

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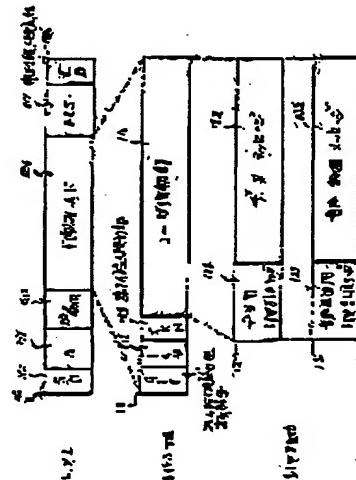
## (54) PACKET EXCHANGE SYSTEM

### (57) Abstract:

**PURPOSE:** To prevent the overflow of a buffer of a receiver of each transmission line by allowing a terminal equipment to execute the retransmission processing with respect to error control so as to transfer a packet in a network at a high speed.

**CONSTITUTION:** A frame is divided into a supervisory field, an information field 104 and a 1st error detection section FCS and the supervisory field consists of an address part A and a receiver operation display part RR/RNR. A packet 11 transferred between terminal equipments is transmitted onto each transmission line by the information field 104 of the frame 10, the packet 11 consists of a format identifier GFI, a logic channel number LCN, a 2nd error detection part CKH and user information part 114, and the GFI acts like a frame error detection processing identifier. An exchange station in the network uses the header part only comprising the GFI, LCN and CKH to apply packet exchange processing.

The packets 12, 13 have respectively a data header 121 and a voice/ picture header 131, but the header processing is applied by terminal equipments only and the exchange station does not participate therein.



### LEGAL STATUS

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